ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	MANAGING A SECURE ENVIRONMENT USING A CHIPSET IN ISOLAQTED EXECUTION MODE

Application Number : 09/668408

Confirmation Number: 2377

First Named Applicant: Carl Ellison

Attorney Docket Number: P8628X

Art Unit: 2131

Examiner: Taghi T Arani

Search string: (3996449 or 4430709 or 4621318 or 5944821 or 5956408 or 6044478 or 6175924 or 6327652 or 6615278 or 6678825 or 20010027511 or 20020007456 or 20020023032 or 20020147916 or 20020166061 or 20020169717 or 20030074548 or 20030115453 or 20030126442 or 20030126453 or 20030159056 or 20030188179 or 20030196085 or 20040117539).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
T.A	1	3996449	1976-12-07	ATTANASIO			
T.A	2	4430709	1984-02-07	SCHLEUPEN			
	3	4621318	1986-11-04	MAEDA			
	4	5944821	1999-08-31	ANGELO			
	5	5956408	1999-09-21	ARNOLD			
	6	6044478	2000-03-28	GREEN			
	7	6175924	2001-01-16	ARNOLD			
	8	6327652	2001-12-04	ENGLAND			
	9	6615278	2003-09-02	CURTIS			
T.A	10	6678825	2004-01-13	ELLISON			

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
T.A	1	20010027511	2001-10-04	WAKABAYASHI			
T.A	2	20020007456	2002-01-17	PEINADO			
T.A	3	20020023032	2002-02-21	PEARSON			
T.A	4	20020147916	2002-10-10	STRONGIN			

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known	
Application Number	09/668,408
Filing Date	September 22, 2000
First Named Inventor	Carl M. Ellison
Art Unit	2131
Examiner Name	Taghi T. Arani
Attorney Docket Number	42390P8628X

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number: Number - Kind Code[2] (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TA	1	US-5303378	04-12-1994	COHEN	
TA	2	US-5522075	05-28-1996	ROBINSON et al	
TA	3	US-5668971	09-16-1997	NEUFELD	
TA	4	US-5898883	04-27-1999	FUJII et al	
TA	5	US-5950221	09-07-1999	DRAVES et al	
TA	6	US-6226749	05-01-2001	CARLOGANU et al	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document: Country Code[3] - Number[4] - Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

Examiner Signature	Taghi T. Arani	Date Considered	01/16/05

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

[1]Applicant's unique citation designation number (optional). [2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). [4]For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. [5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. [6]Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Sokoloff, Taylor & Zafman (wtr) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

T.A	5	20020166061	2002-11-07	FALIK		—	
T.A	6	20020169717	2002-11-14	CHALLENER		—	
	7	20030074548	2003-04-17	CROMER		—	
	8	20030115453	2003-06-19	GRAWROCK			
	9	20030126442	2003-07-03	GLEW		—	
	10	20030126453	2003-07-03	GLEW			
	11	20030159056	2003-08-21	CROMER		—	
	12	20030188179	2003-10-02	CHALLENER		—	
	13	20030196085	2003-10-16	LAMPSON		—	
T.A	14	20040117539	2004-06-17	BENNETT			

Signature Joan T. Adams

Examiner Name	Date
	6/16/05

Reference No.	Application Serial No.	Patent No.	Title
1	09/672,603		Controlling Accesses To Isolated Memory Using A Memory Controller For Isolated Execution
2	09/822,986		Checking File Integrity Using Signature Generated In Isolated Execution
3	09/538,951		Platform And Method For Issuing And Certifying A Hardware-Protected Attestation Key
4	09/539,344		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
5	09/668,585		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
6	09/541,108		Platform And Method For Remote Attestation Of A Platform
7	09/540,612		Platform And Method For Generating And Utilizing A Protected Audit Log
8	09/540,946		Protecting Software Environment In Isolated Execution
9	09/668,610		Protecting Software Environment In Isolated Execution
10	09/538,954		Generating Isolated Bus Cycles For Isolated Execution
11	09/539,348	6,760,441	Generating A Key Hierarchy For Use In An Isolated Execution Environment
12	09/541,477	6,507,904	Isolated Instructions For Isolated Execution
13	09/540,611		Managing Accesses In A Processor For Isolated Execution
14	09/540,613		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
15	09/668,408		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
16	09/541,667		Attestation Key Memory Device And Bus
17	09/672,602		Attestation Key Memory Device And Bus
18	09/618,738	6,678,825	Controlling Access To Multiple Isolated Memories In An Isolated Execution Environment
19	09/618,489	6,633,963	Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
20	10/683,542		Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
21	09/618,659		Resetting A Processor In An Isolated Execution Environment
22	09/751,586		Resetting A Processor In An Isolated Execution Environment